**Boolean Algebra**

**Exercise 1** - Write a boolean equation in sum-of-products canonical form for each of the truth tables, then draw the combinatorial circuit and simulate it on Deeds to check the correctness.

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| **A** | **B** | **OUT** |
| 0 | 0 | **1** |
| 0 | 1 | **0** |
| 1 | 0 | **0** |
| 1 | 1 | **0** |

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| **X1** | **X2** | **X3** | **Y** |
| 0 | 0 | 0 | **0** |
| 0 | 0 | 1 | **0** |
| 0 | 1 | 0 | **1** |
| 0 | 1 | 1 | **1** |
| 1 | 0 | 0 | **0** |
| 1 | 0 | 1 | **1** |
| 1 | 1 | 0 | **1** |
| 1 | 1 | 1 | **0** |

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| **A** | **B** | **C** | **Y** |
| 0 | 0 | 0 | **0** |
| 0 | 0 | 1 | **0** |
| 0 | 1 | 0 | **0** |
| 0 | 1 | 1 | **0** |
| 1 | 0 | 0 | **0** |
| 1 | 0 | 1 | **1** |
| 1 | 1 | 0 | **1** |
| 1 | 1 | 1 | **1** |

a)

b) c)

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| **W** | **X** | **Y** | **Z** | **F** |
| 0 | 0 | 0 | 0 | **0** |
| 0 | 0 | 0 | 1 | **1** |
| 0 | 0 | 1 | 0 | **0** |
| 0 | 0 | 1 | 1 | **0** |
| 0 | 1 | 0 | 0 | **0** |
| 0 | 1 | 0 | 1 | **1** |
| 0 | 1 | 1 | 0 | **0** |
| 0 | 1 | 1 | 1 | **0** |
| 1 | 0 | 0 | 0 | **0** |
| 1 | 0 | 0 | 1 | **1** |
| 1 | 0 | 1 | 0 | **0** |
| 1 | 0 | 1 | 1 | **0** |
| 1 | 1 | 0 | 0 | **0** |
| 1 | 1 | 0 | 1 | **1** |
| 1 | 1 | 1 | 0 | **1** |
| 1 | 1 | 1 | 1 | **1** |

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| **W** | **X** | **Y** | **Z** | **F** |
| 0 | 0 | 0 | 0 | **0** |
| 0 | 0 | 0 | 1 | **0** |
| 0 | 0 | 1 | 0 | **1** |
| 0 | 0 | 1 | 1 | **1** |
| 0 | 1 | 0 | 0 | **0** |
| 0 | 1 | 0 | 1 | **1** |
| 0 | 1 | 1 | 0 | **0** |
| 0 | 1 | 1 | 1 | **1** |
| 1 | 0 | 0 | 0 | **0** |
| 1 | 0 | 0 | 1 | **1** |
| 1 | 0 | 1 | 0 | **1** |
| 1 | 0 | 1 | 1 | **1** |
| 1 | 1 | 0 | 0 | **0** |
| 1 | 1 | 0 | 1 | **0** |
| 1 | 1 | 1 | 0 | **0** |
| 1 | 1 | 1 | 1 | **0** |

d)

e)

**Exercise 2** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.

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| **A** | **B** | **C** | **OUT** |
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Immagine che contiene diagramma, Piano, linea, Disegno tecnico

Descrizione generata automaticamente

Immagine che contiene tavolo

Descrizione generata automaticamente

**Exercise 3** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.

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| **A** | **B** | **F** |
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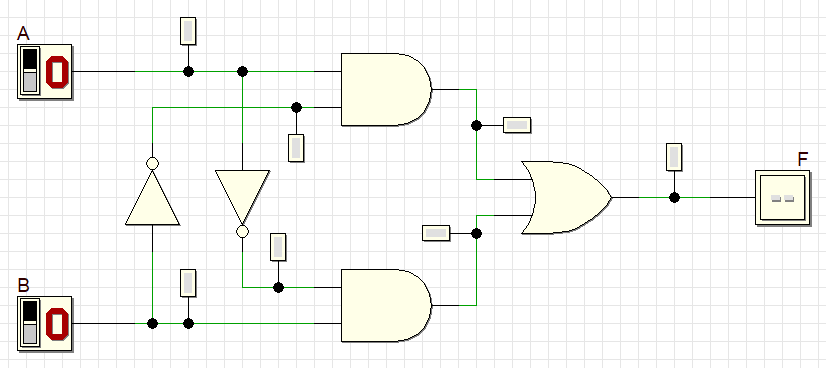
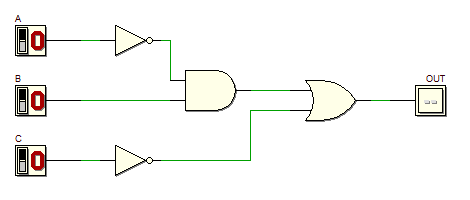


Immagine che contiene testo, linea, schermata, Diagramma

Descrizione generata automaticamente

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| **A** | **B** | **C** | **OUT** |
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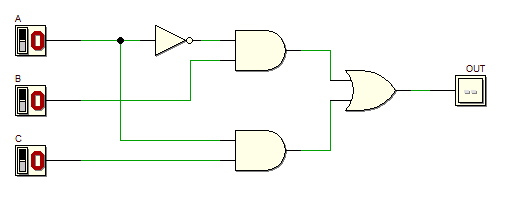
**Exercise 4** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.





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| **A** | **B** | **C** | **OUT** |
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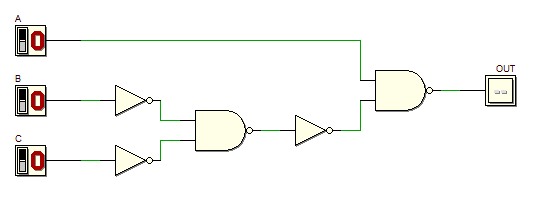
**Exercise 5** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.





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| **A** | **B** | **C** | **OUT** |
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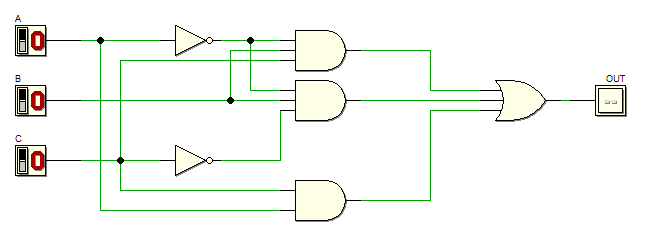
**Exercise 6** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.





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| **A** | **B** | **C** | **OUT** |
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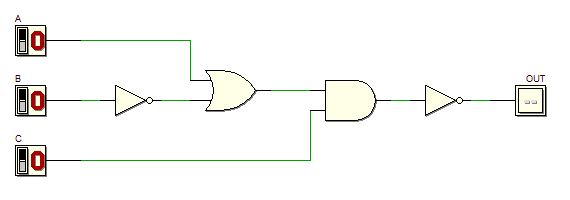
**Exercise 7** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.





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| **A** | **B** | **C** | **OUT** |
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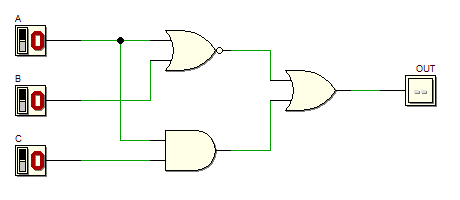
**Exercise 8** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.





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| **A** | **B** | **C** | **OUT** |
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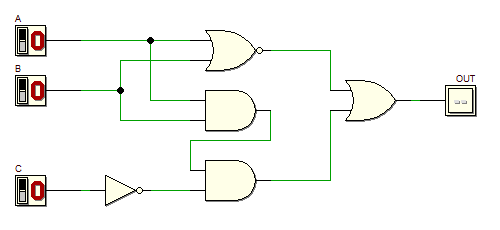
**Exercise 9** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.





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| **A** | **B** | **C** | **OUT** |
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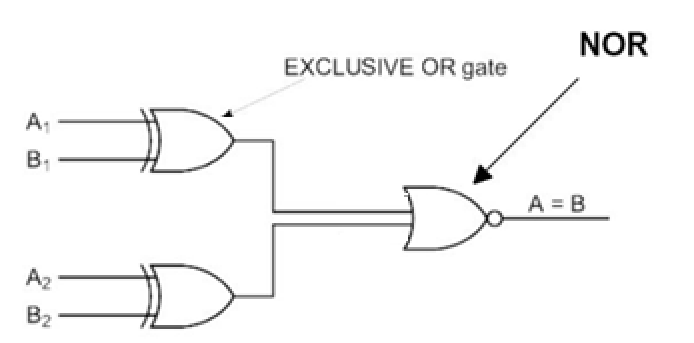
**Exercise 10** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.





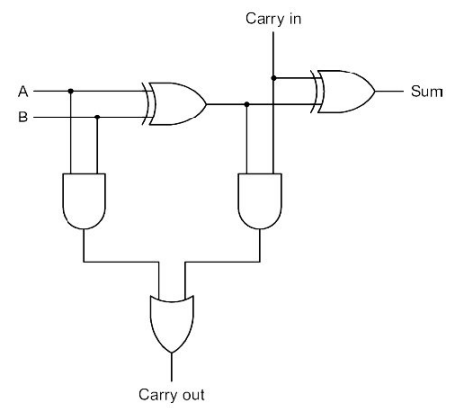
**Exercise 11** - Fill the truth table; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.   
This is a 2-BIT COMPARATOR, it checks whether the value A2A1 is equal to B2B1.

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| **A1** | **B1** | **A2** | **B2** | **OUT** |
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**Exercise 12** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers. This is a 1-BIT SUMMATOR, it sums bit A, bit B and the input carry; it generates the sum and the carry output.

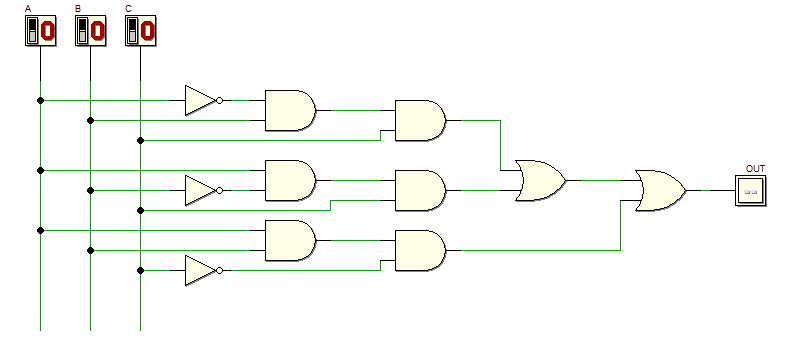
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| **A** | **B** | **CarryIn** | **Sum** | **CarryOut** |
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 Immagine che contiene tavolo

Descrizione generata automaticamente

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| **A** | **B** | **C** | **OUT** |
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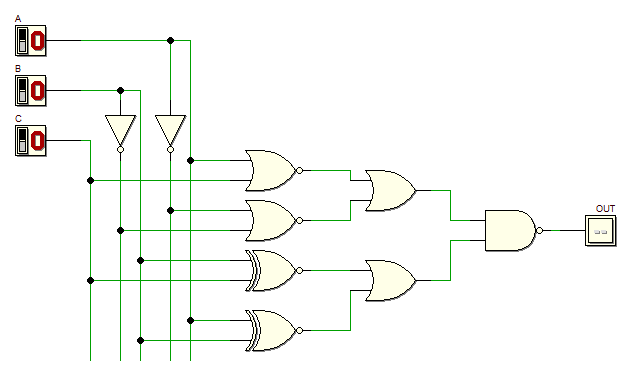
**Exercise 13** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.





**Exercise 14** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.

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| **A** | **B** | **C** | **OUT** |
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**Exercise 15** - Fill the truth table and the time diagram; then write the boolean equation in sum-of-product or product-of-sum canonical form; finally design the circuit using DEEDS to check the correctness of your answers.

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| **A** | **B** | **C** | **OUT** |
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